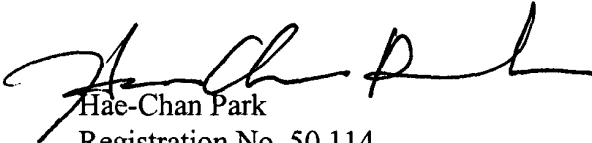


connected to the drain electrode through the contact hole is formed on the passivation layer. Here, the opening is located between the data line and the pixel electrode. In this structure, misalignment occurring in the manufacturing process of a thin film transistor panel for a liquid crystal display is minimized, and stitch defects are prevented by uniformity forming a coupling capacitance between the data line and the pixel electrode. Shorts between the data line and the pixel electrode are prevented by forming the opening between the data line and the pixel electrode.

### ***Conclusion***

It is respectfully requested that this amendment be entered prior to the examination of the above-referenced patent application. It is believed that no new matter is added by this amendment. By this amendment, claims 14-17 and 20-29 are pending, among which claims 14, 17 and 20 are independent claims. If the Examiner desires any additional information, the Examiner is invited to contact applicants' attorney at the telephone number listed below to expedite prosecution.

Respectfully submitted,

  
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